

**DP133GT**

# Specification

**DP****DOPOINT****133****Size****G****Pixel**

- A:640\*480
- B:800\*480
- C:800\*600
- D:1024\*600
- E:1024\*768
- F:1280\*768
- G:1280\*800
- H:1280\*1024
- HD:1366\*768
- J:1600\*1200
- K:1920\*1200
- FHD:1920\*1080

**T****Mode**

- T: TN
- I: IPS

Product Order Number	Description
DP133GT-XXXX	<ul style="list-style-type: none"> <li>• TN Normally White</li> <li>• -30°C to +70°C Operating Temperature</li> <li>• Viewing Angle L/R/U/D - 70/70/60/60</li> <li>• Luminance – 1100 cd/m<sup>2</sup></li> <li>• Contrast Ratio – 800:1</li> </ul>
DP133GT-TXXX	Resistive + Capacitive Touch by Optical Bonding
DP133GT-HXXX	Extended temp (Add Heater by Optical Bonding) -40°C to +80°C Operating Temperature
DP133GT-EXXX	Integrated Front Optical EMI Filter by Optical Bonding
DP133GT-ARAG	Anti-Reflective + Anti-Glare Surface Treatment
DP133GT-NVIS	Integrated NVIS Optical Filter

**Sunlight Readable LCDs With Optical Bonding + Touch Integration**



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## 1. GENERAL DESCRIPTION

### 1.1 OVERVIEW

DP133GT is a 13.3" TFT Liquid Crystal Display module and 20 pins LVDS interface. This module supports 1280 x 800 WXGA mode and can display 262K or 16.2M colors. The converter circuit for LED is built in.

### 1.2 FEATURES

- WXGA (1280 x 800 pixels) resolution
- LVDS (Low Voltage Differential Signaling) interface

### 1.3 APPLICATION

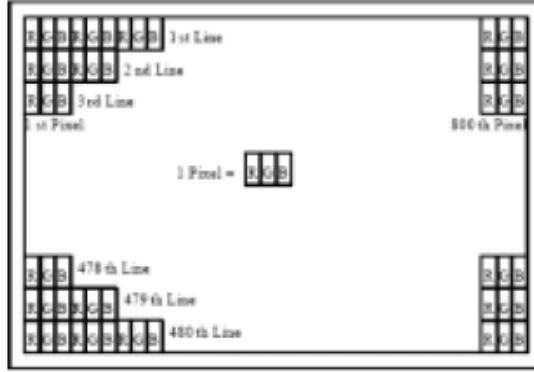
- Industry Application

### 1.4 GENERAL SPECIFICATIONS

Item	Specification	Unit	Note
Diagonal Size	13.3	inch	
Active Area	286.08 x 178.8	mm	(1)
Bezel Opening Area	289.1 x 181.8	mm	
Driver Element	a-si TFT active matrix	-	-
Pixel Number	1280 x R.G.B. x 800	pixel	-
Pixel Pitch	0.2235 x 0.2235	mm	-
Pixel Arrangement	RGB vertical stripe	-	(2)
Display Colors	262k or 16.2M	color	-
Display Mode	Normally White	-	-
Surface Treatment	Anti-glare, Hard Coating ( 3H )	-	-
Module Power Consumption	(6.5)	W	Typ.

Note (1) Please refer to the attached drawings for more information of front and back outline dimensions.

Note (2)



### 1.5 MECHANICAL SPECIFICATIONS

Item	Min.	Typ.	Max.	Unit	Note
Module Size	Horizontal (H)	298.5	299	mm	(1)
	Vertical (V)	194.5	195	mm	
	Depth (D)	-	(7.4)	mm	
Weight	-	(365)	(380)	g	

Note (1) Please refer to the attached drawings for more information of front and back outline dimensions.

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## 2. ABSOLUTE MAXIMUM RATINGS

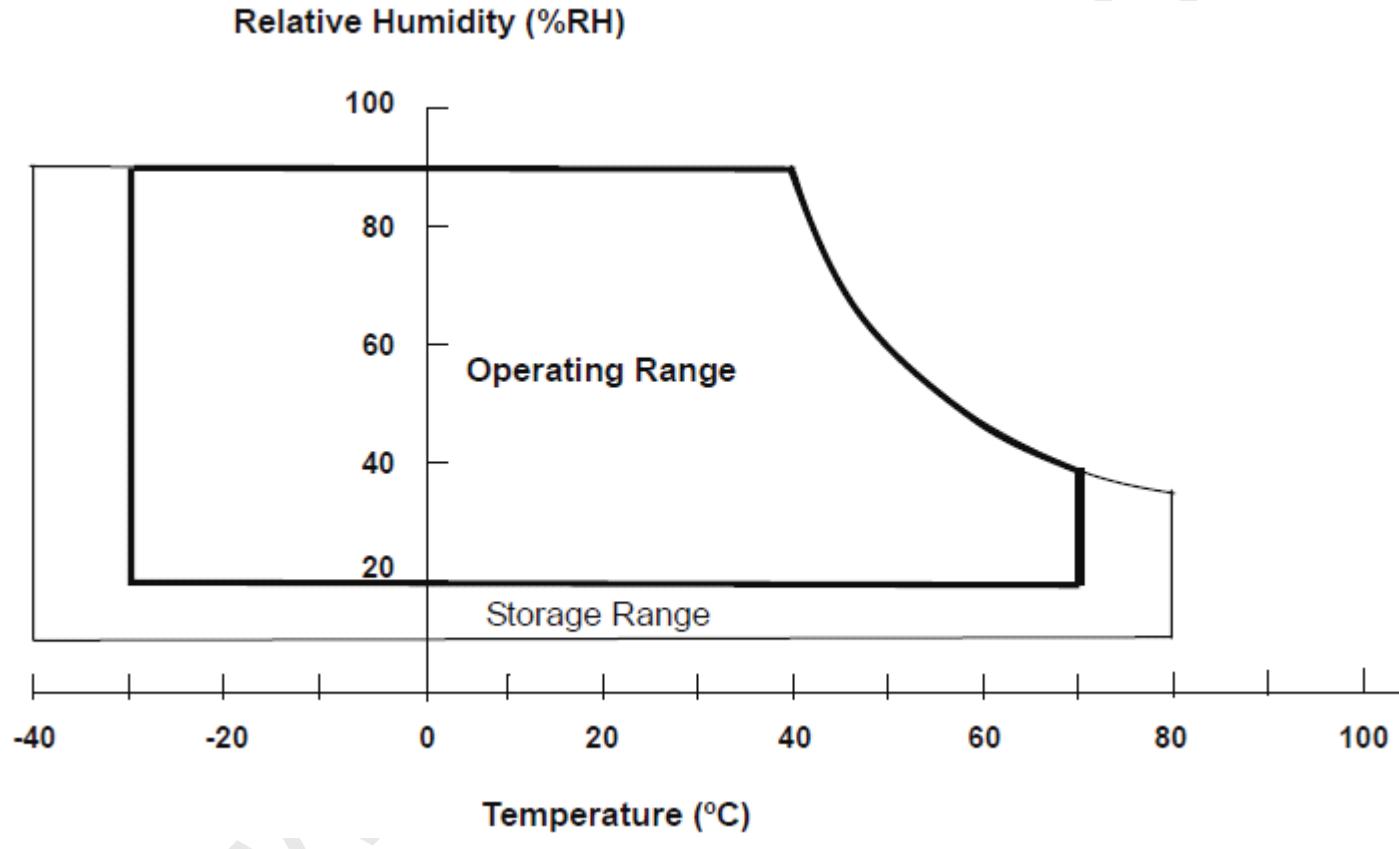
### 2.1 ABSOLUTE RATINGS OF ENVIRONMENT

Item	Symbol	Value		Unit	Note
		Min.	Max.		
Operating Ambient Temperature	$T_{OP}$	-30	+70	°C	
Storage Temperature	$T_{ST}$	-30	+80	°C	

Note (1) Temperature and relative humidity range is shown in the figure below.

(2) Wet-bulb temperature should be 39 °C Max. ( $T_a > 40$  °C).

(3) No condensation.



### 2.2 ELECTRICAL ABSOLUTE RATINGS

#### 2.2.1 TFT LCD MODULE

$T_a = 25 \pm 2$  °C

Item	Symbol	Value		Unit	Note
		Min.	Max.		
Power Supply Voltage	Vcc	(-0.3)	(4)	V	(1)

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## 2.2.2 LED CONVERTER

Item	Symbol	Value		Unit	Note
		Min.	Max.		
Converter Voltage	V <sub>i</sub>	(4.5)	(24)	V	(1), (2)
Enable Voltage	EN	---	(5.5)	V	
Backlight Adjust	ADJ	---	(5.5)	V	

Note (1) Permanent damage to the device may occur if maximum values are exceeded. Function operation should be restricted to the conditions described under Normal Operating Conditions.

Note (2) Specified values are for LED converter (Refer to 3.2 for further information).

## 3. ELECTRICAL CHARACTERISTICS

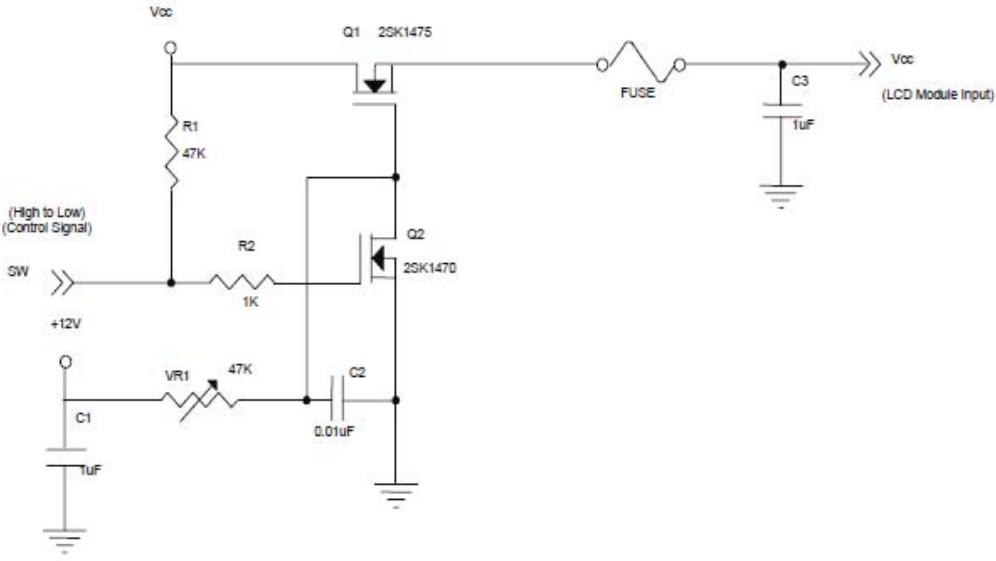
### 3.1 RECOMMENDED OPERATION CONDITION

T<sub>a</sub> = 25 ± 2 °C

Parameter	Symbol	Value			Unit	Note
		Min.	Typ.	Max.		
Power Supply Voltage	V <sub>CC</sub>	(3.0)	(3.3)	(3.6)	V	-
Permissive Ripple Voltage	V <sub>RP</sub>		(50)		mV	-
Rush Current	I <sub>RUSH</sub>			(1.5)	A	(2)
Initial Stage Current	I <sub>IS</sub>			(1.0)	A	(2)
Power Supply Current	White	I <sub>CC</sub>	(330)	(370)	mA	(3)a
	Black					
LVDS Differential Input High Threshold		V <sub>TH(LVDS)</sub>		(+100)	mV	(4), V <sub>CM</sub> =1.2V
LVDS Differential Input Low Threshold		V <sub>TL(LVDS)</sub>	(-100)		mV	(4) V <sub>CM</sub> =1.2V
LVDS Common Mode Voltage		V <sub>CM</sub>	(0.7)		V	(4)
LVDS Differential Input Voltage		V <sub>ID</sub>	(100)		mV	(4)
Terminating Resistor		R <sub>T</sub>		(100)	Ohm	

Note (1) The assembly should be always operated within above ranges.

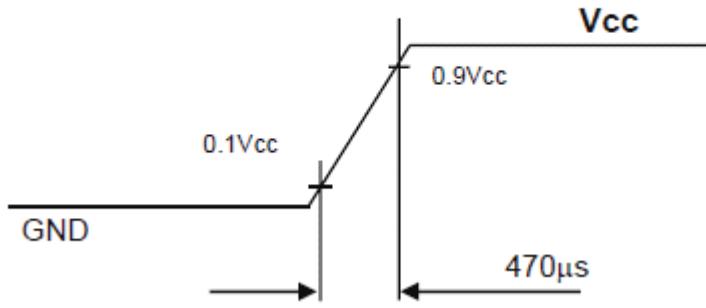
Note (2) Measurement Conditions:



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V<sub>cc</sub> rising time is 470μs



Note (3) The specified power supply current is under the conditions at  $V_{cc} = 3.3V$ ,  $T_a = 25 \pm 2 ^\circ C$ ,  $f_v = 60 Hz$ , whereas a power dissipation check pattern below is displayed.

a. White Pattern



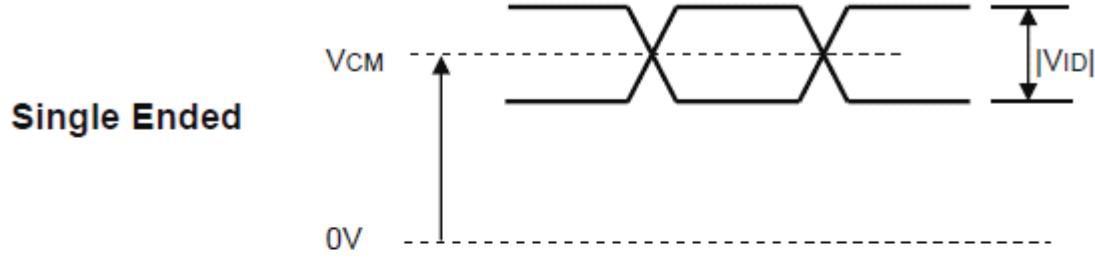
Active Area

b. Black Pattern



Active Area

Note (4) The parameters of LVDS signals are defined as the following figures.



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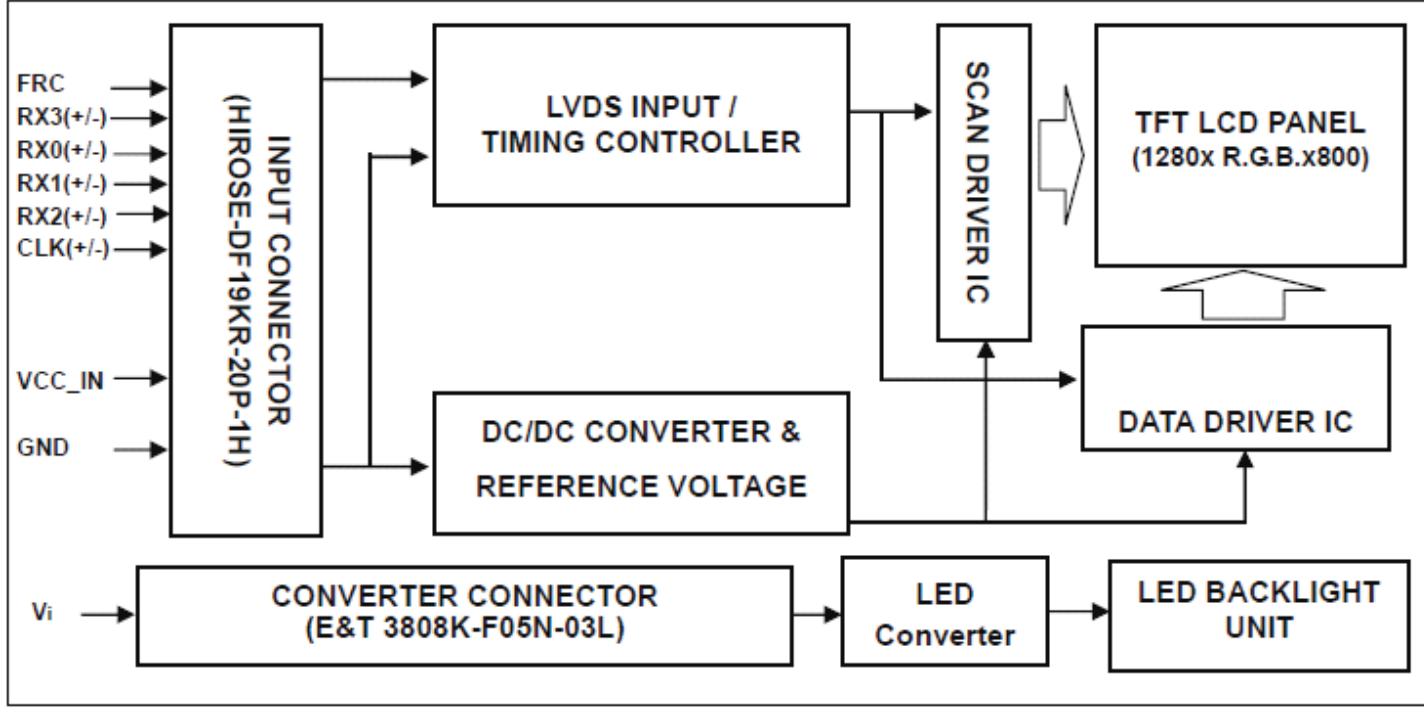
## 3.2 BACKLIGHT UNIT

T<sub>a</sub> = 25 ± 2 °C

Parameter	Symbol	Value			Unit	Note
		Min.	Typ.	Max.		
Converter Power Supply Voltage	V <sub>i</sub>	(10.8)	(12.0)	(13.2)	V	
Converter Power Supply Current	I <sub>i</sub>	---	(0.43)	---	A	@ V <sub>i</sub> = 12V (Duty 100%)
Converter Power Consumption	P <sub>LED</sub>	---	(5.16)	---	W	@ V <sub>i</sub> = 12V (Duty 100%)
EN Control Level	Backlight on	(1.6)	---	(5)	V	
	Backlight off		---	(1)	V	
PWM Control Level	PWM High Level	(1.3)	---	(5)	V	
	PWM Low Level		---	(0.65)	V	
PWM Control Duty Ratio		(10)		(100)	%	
PWM Control Frequency	f <sub>PWM</sub>	(100)	(200)	(10K)	Hz	
LED Life Time	L <sub>L</sub>	(30,000)			Hrs	(2)

## 4. BLOCK DIAGRAM

### 4.1 TFT LCD MODULE



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## 5. INPUT TERMINAL PIN ASSIGNMENT

### 5.1 LVDS I/O PIN ASSIGNMENT

Pin	Symbol	Description	Polarity	Remark
1	Vss	Ground		
2	Vcc	Power Supply +3.3 V (typical)		
3	Vcc	Power Supply +3.3 V (typical)		
4	NC	Non-Connection		
5	FRC	Dithering control setting When FRC=H, the width of data input 8 bits When FRC=L, the width of data input 6 bits and set Dx0 and Dx1 to logical low (Default is L)		H is 3.3V L is GND
6	Rxin3-	LVDS Differential Data Input	Negative	
7	Rxin3+	LVDS Differential Data Input	Positive	
8	Rxin0-	LVDS Differential Data Input	Negative	
9	Rxin0+	LVDS Differential Data Input	Positive	
10	Vss	Ground		
11	Rxin1-	LVDS Differential Data Input	Negative	
12	Rxin1+	LVDS Differential Data Input	Positive	
13	Vss	Ground		
14	Rxin2-	LVDS Differential Data Input	Negative	
15	Rxin2+	LVDS Differential Data Input	Positive	
16	Vss	Ground		
17	CLK-	LVDS Clock Data Input	Negative	LVDS Level Clock
18	CLK+	LVDS Clock Data Input	Positive	
19	Vss	Ground		
20	Vss	Ground		

Note (1) User's connector Part No.: DF19G-20S-1SD (HIROSE) or equivalent DF19G-20S-1For  
DF19G-20S-1C

### 5.2 BACKLIGHT PIN ASSIGNMENT (Converter connector pin)

Pin	Symbol	Description	Remark
1	Vi	Converter input voltage	12V
2	VGND	Converter ground	Ground
3	EN	Enable pin	3.3V
4	ADJ	Backlight Adjust	PWM Dimming (Hi: 3.3VDC, Lo: 0VDC)
5	NC	Not Connect	Ground

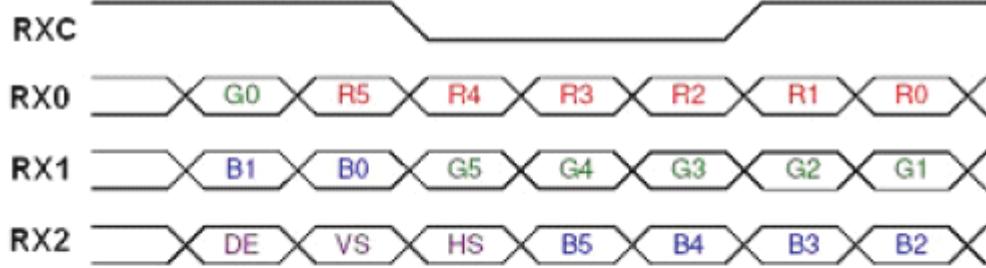
Note (1) User's connector Part No: E&T H208K-P05N-02B or equivalent

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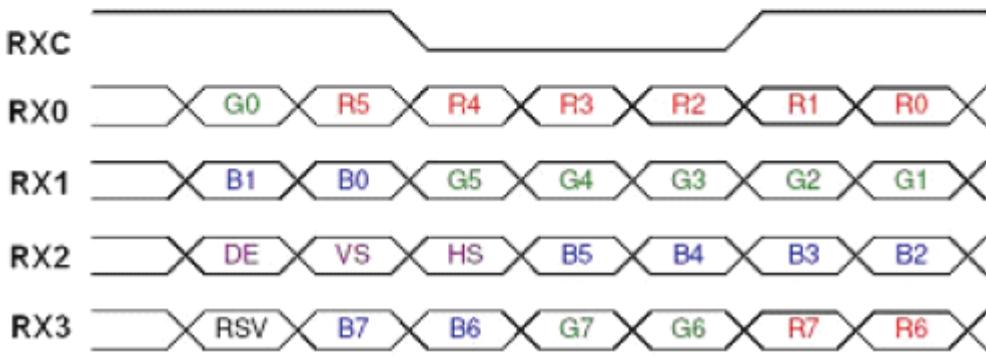
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## 5.3 TIMING DIAGRAM OF LVDS INPUT SIGNAL

**FRC** = "Low" or "NC" for 6 bits LVDS Input



**FRC** = "High" for 8 bits LVDS Input



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## 5.4 COLOR DATA INPUT ASSIGNMENT

The brightness of each primary color (red, green and blue) is based on the 6-bit gray scale data input for the color. The higher the binary input, the brighter the color. The table below provides the assignment of color versus data input. ( 0: Low Level Voltage, 1: High Level Voltage)

Color		Data Signal																	
		Red						Green						Blue					
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
Basic Colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray Scale Of Red	Red(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(2)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red(61)	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale Of Green	Green(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green(61)	0	0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0
	Green(62)	0	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0
	Green(63)	0	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
Gray Scale Of Blue	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(61)	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0
	Blue(62)	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0
	Blue(63)	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:

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The brightness of each primary color (red, green and blue) is based on the 8-bit gray scale data input for the color.

The higher the binary input, the brighter the color. The table below provides the assignment of color versus data input.

( 0: Low Level Voltage, 1: High Level Voltage)

Color		Data Signal																							
		Red								Green								Blue							
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
Basic Colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
Gray Scale Of Red	Red(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Red(1)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Red(2)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:		
	Red(253)	1	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Red(254)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
Gray Scale Of Green	Green(0)/ Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	
	Green(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:		
	Green(253)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	1	0	0	0	0	0	
	Green(254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	
Gray Scale Of Blue	Blue(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:		
	Blue(253)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	
	Blue(254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	
	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	

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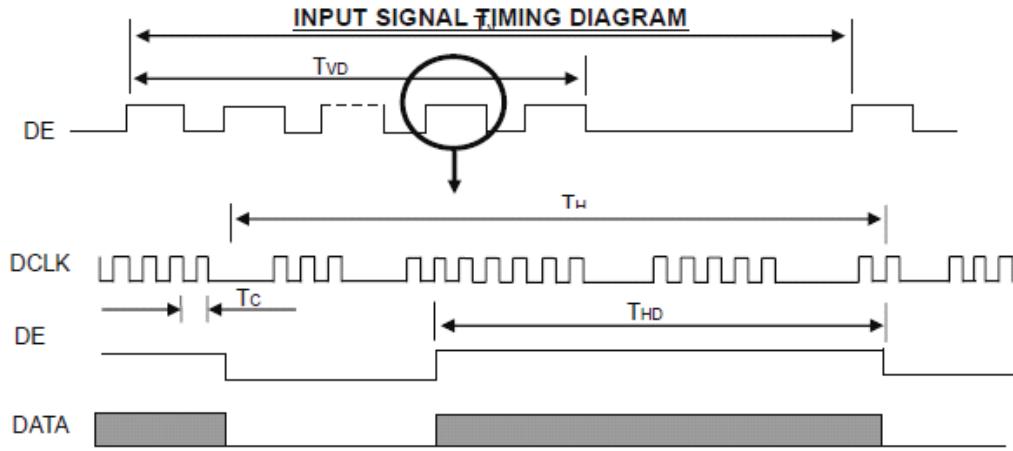
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## 6. INTERFACE TIMING

### 6.1 TIMING CHARACTERISTICS

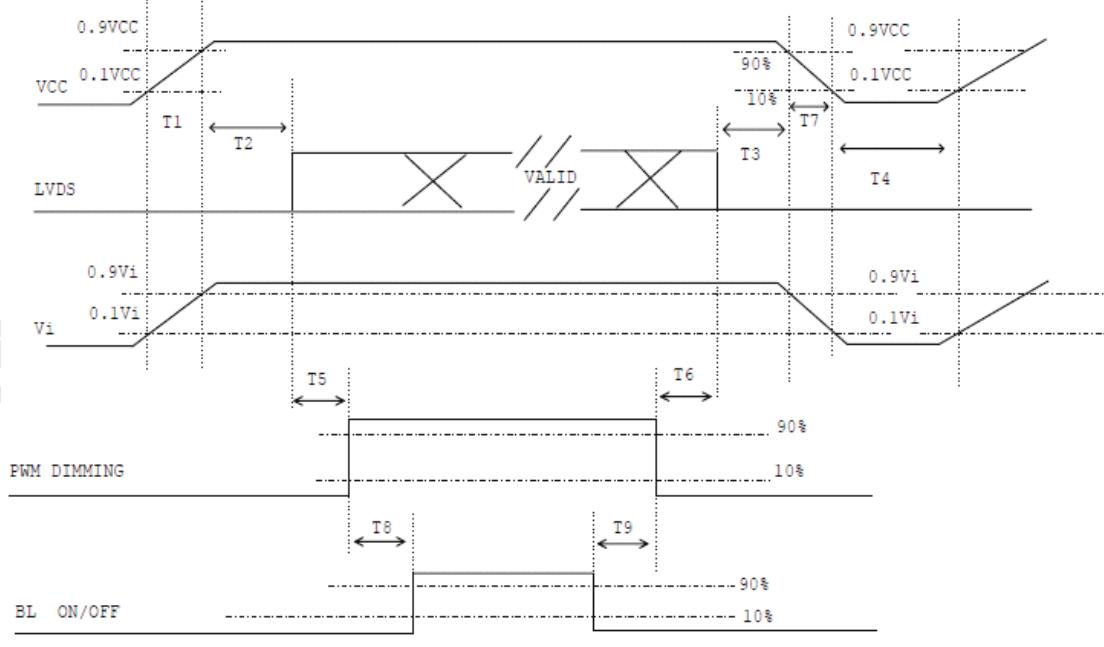
The specifications of input signal timing are as the following table and timing diagram.

Signal	Item	Symbol	Min.	Typ.	Max.	Unit	Note
DCLK	Frequency	1/Tc	(50)	(71)	(80)	MHz	-
DE	Vertical Total Time	TV	(810)	(823)	(1023)	TH	-
DE	Vertical Addressing Time	TVD	(800)	(800)	(800)	TH	-
DE	Horizontal Total Time	TH	(1360)	(1440)	(1800)	Tc	-
DE	Horizontal Addressing Time	THD	(1280)	(1280)	(1280)	Tc	-



### 6.2 POWER ON/OFF SEQUENCE

To prevent a latch-up or DC operation of LCD assembly, the power on/off sequence should be as the diagram Below



**Power ON/OFF sequence**

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Note (1) Please avoid floating state of interface signal at invalid period.

Note (2) When the interface signal is invalid, be sure to pull down the power supply of LCD VCC to 0 V.

Note (3) The Backlight converter power must be turned on after the power supply for the logic and the interface signal is valid. The Backlight converter power must be turned off before the power supply for the logic and the interface signal is invalid.

## 7. OPTICAL CHARACTERISTICS

### 7.1 TEST CONDITIONS

Item	Symbol	Value	Unit
Ambient Temperature	T <sub>a</sub>	25±2	°C
Ambient Humidity	H <sub>a</sub>	50±10	%RH
Supply Voltage	V <sub>cc</sub>	3.3	V
Input Signal	According to typical value in "3. ELECTRICAL CHARACTERISTICS"		
Current	I <sub>f</sub>	20±3	mA
Converter Duty		100	%

Note (1) If means the forward current of each channel

### 7.2 OPTICAL SPECIFICATIONS

The relative measurement methods of optical characteristics are shown in 7.2. The following items should be measured under the test conditions described in 7.1 and stable environment shown in Note (6).

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Color Chromaticity	Rx	θ <sub>x</sub> =0°, θ <sub>y</sub> =0° Viewing Normal Angle		(0.606)			(1), (6)
	Ry			(0.342)			
	Gx			(0.338)			
	Gy			(0.541)			
	Bx			(0.158)			
	By			(0.144)			
	Wx			(0.313)			
	Wy			(0.329)			
Center Luminance of White	L <sub>c</sub>		(900)	(1000)		cd/m <sup>2</sup>	(4), (6)
Contrast Ratio	CR		(550)	(800)		-	(2), (6)
Response Time	T <sub>R</sub>			(6)	(11)	Ms	(3)
	T <sub>F</sub>			(10)	(15)	Ms	
White Variation	δW			(1.25)	(1.4)	-	(5), (6)
Horizontal	θ <sub>x</sub> +		(60)	(70)			
	θ <sub>x</sub> -		(60)	(70)			
	θ <sub>y</sub> +		(50)	(60)			

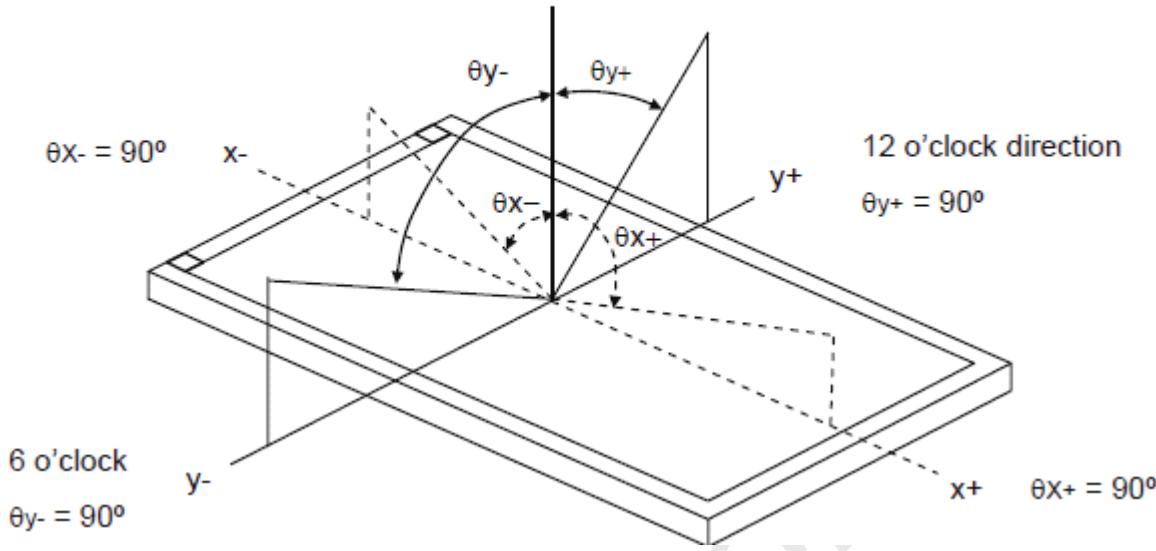
Note (1) Definition of Viewing Angle (θ<sub>x</sub>, θ<sub>y</sub>):

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Normal

$$\theta x = \theta y = 0^\circ$$



Note (2) Definition of Contrast Ratio (CR):

The contrast ratio can be calculated by the following expression.

$$\text{Contrast Ratio (CR)} = L_{63} / L_0$$

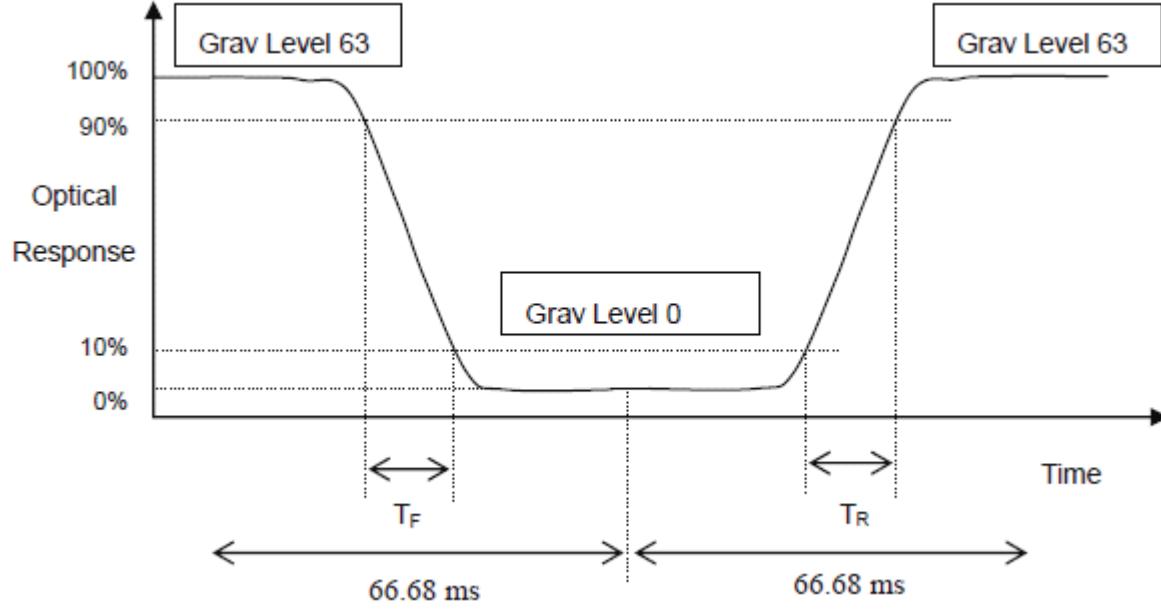
L63: Luminance of gray level 63

L0: Luminance of gray level 0

$$CR = CR(5)$$

CR (X) is corresponding to the Contrast Ratio of the point X at Figure in Note (5).

Note (3) Definition of Response Time (TR, TF) and measurement method:



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Note (4) Definition of Luminance of White (LC):

Measure the luminance of gray level 63 at center point

$$LC = L(5)$$

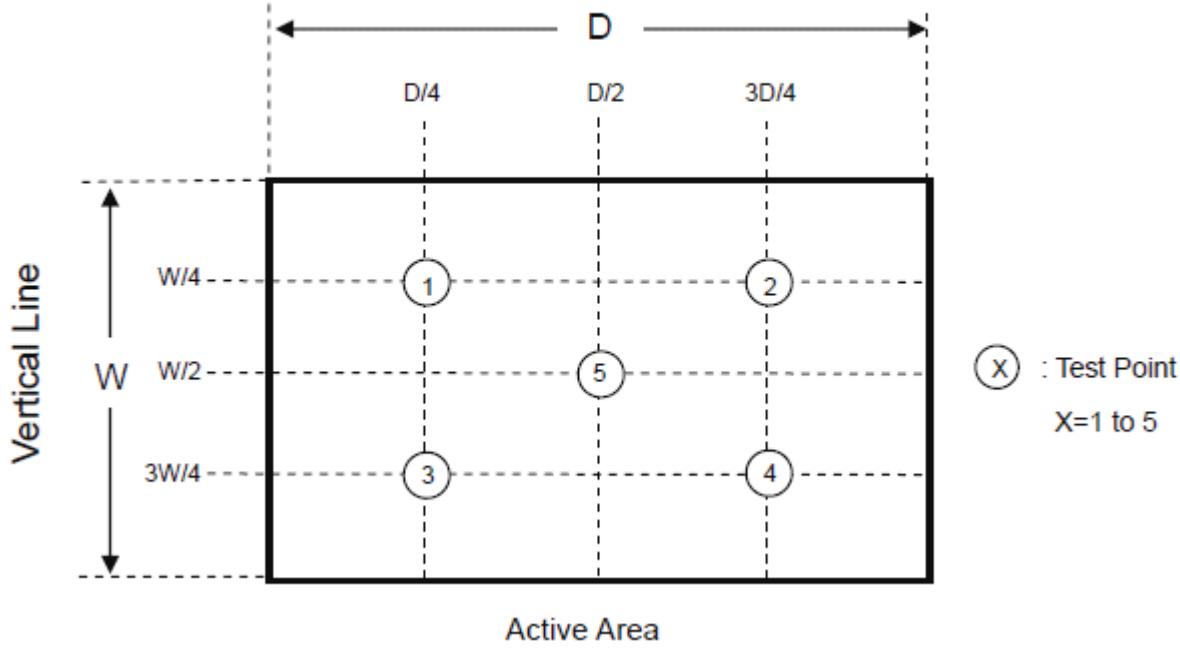
$L(x)$  is corresponding to the luminance of the point X at Figure in Note (5).

Note (5) Definition of White Variation ( $\delta W$ ):

Measure the luminance of gray level 63 at 5 points

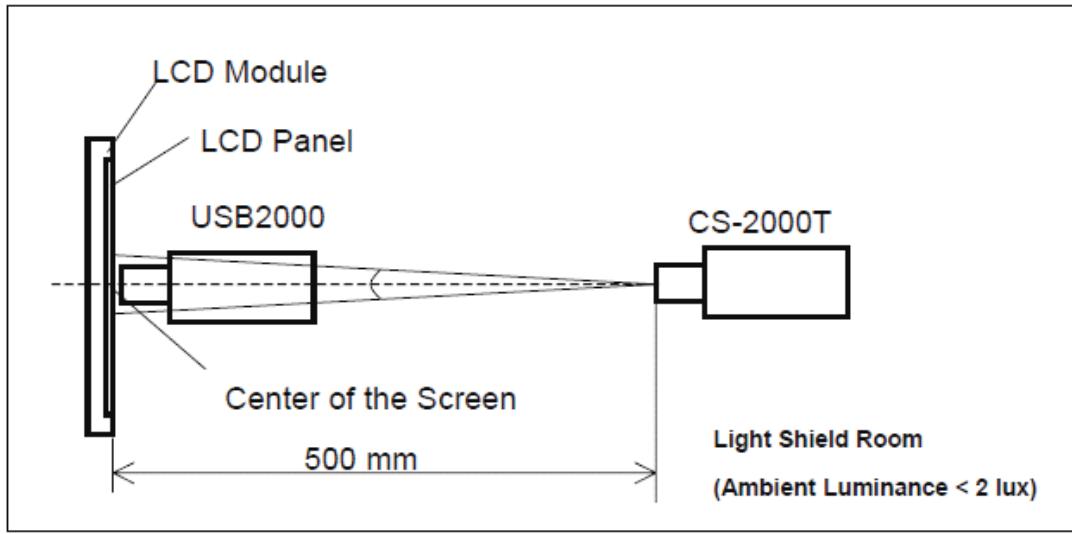
$$\delta W = \text{Maximum} [L(1), L(2), L(3), L(4), L(5)] / \text{Minimum} [L(1), L(2), L(3), L(4), L(5)]$$

**Horizontal Line**



Note (6) Measurement Setup:

The LCD module should be stabilized at given temperature for 20 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 20 minutes in a windless room.



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## 8. RELIABILITY TEST

### 8.1 RELIABILITY TEST CONDITION

## 9. PACKAGING

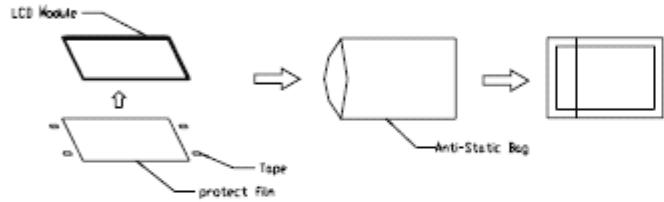
### 9.1 PACKING SPECIFICATIONS

- (1) 20pcs LCD modules / 1 Box
- (2) Box dimensions: 520(L)x310(W)x415(H)mm
- (3) Weight: approximately 13 Kg (20modules per box)

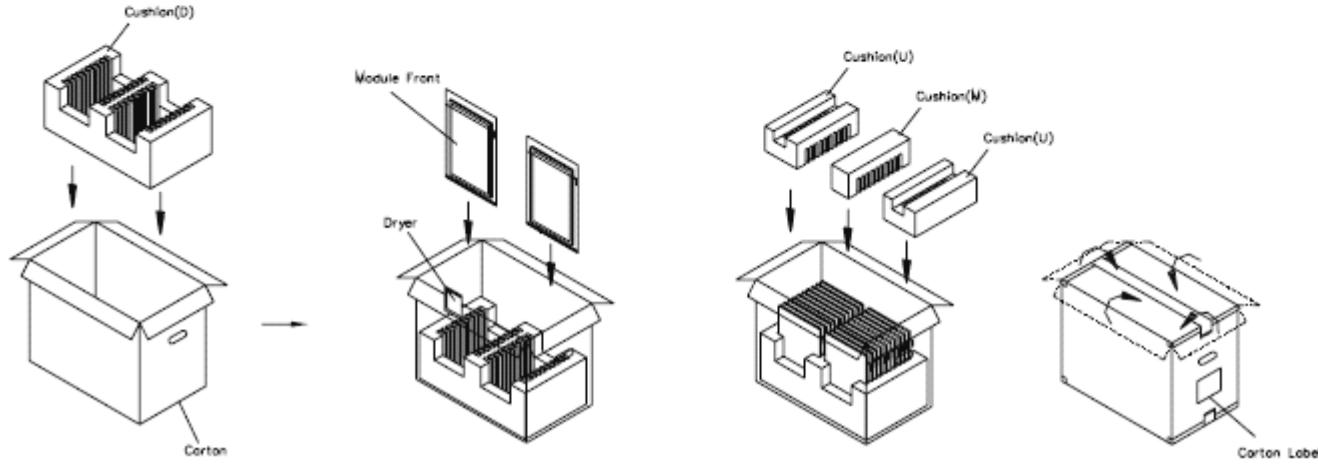
### 9.2 PACKING METHOD

- (1) Carton Packing should have no failure in the following reliability test items.

Test Item	Test Conditions	Note
Vibration	ISTA STANDARD Random, Frequency Range: 2 – 200 Hz Top & Bottom: 30 minutes (+Z), 10 min (-Z), Right & Left: 10 minutes (X) Back & Forth 10 minutes (Y)	Non Operation
Dropping Test	1 Angle, 3 Edge, 6 Face, 61 cm	Non Operation



(1) 20pcs Modules/1 box  
(2) Carton dimensions : 520(L)x310(W)x415(H)mm

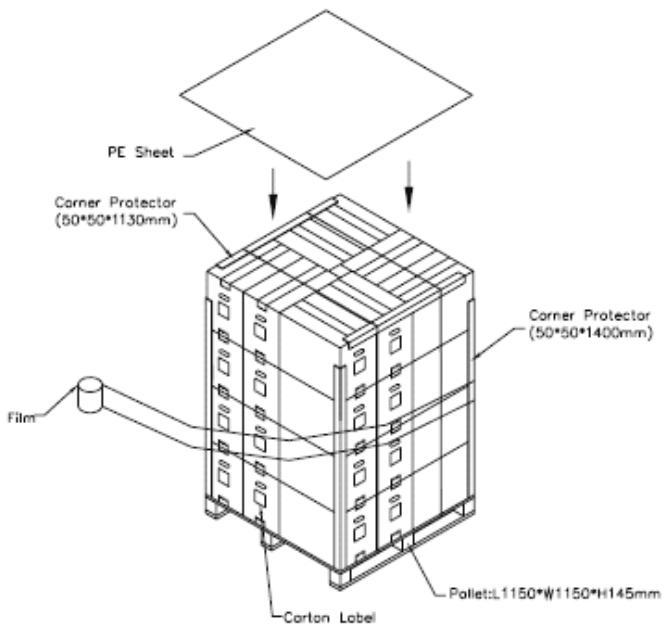


**Figure. 9-1 Packing method**

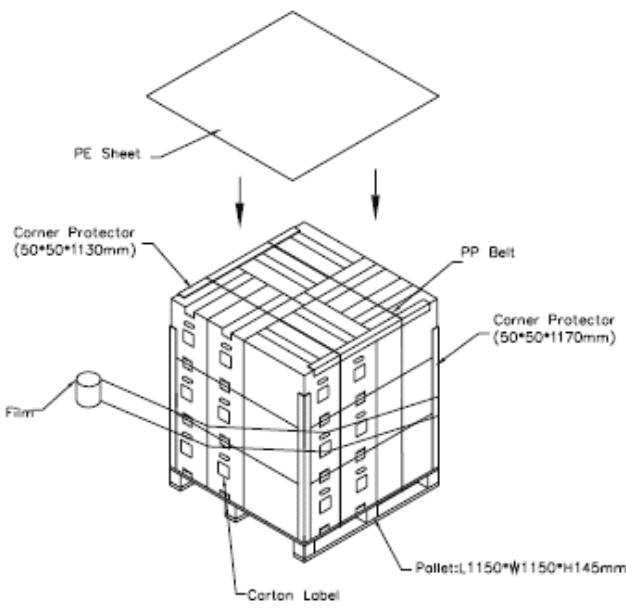
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Sea and Land Transportation



Air Transportation



**Figure. 9-2 Packing method**

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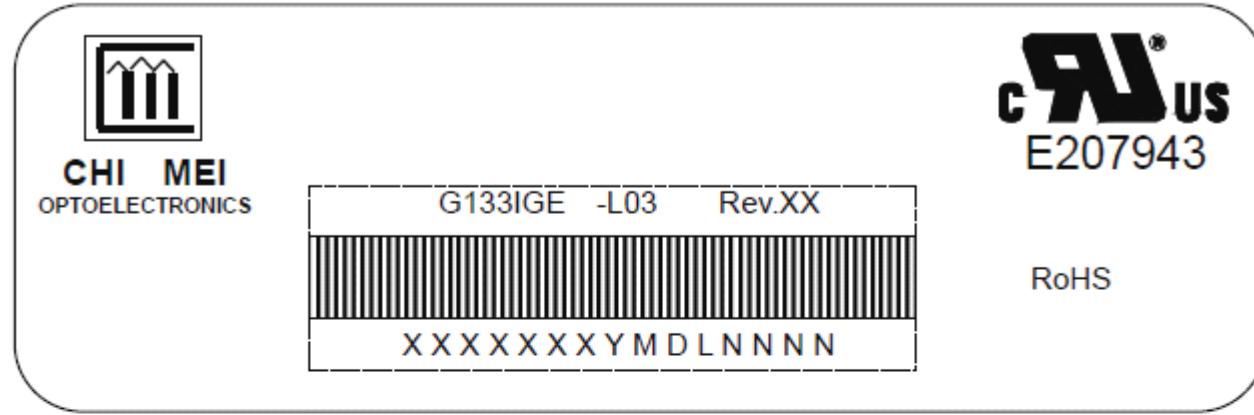

**DP133GT**

# Specification

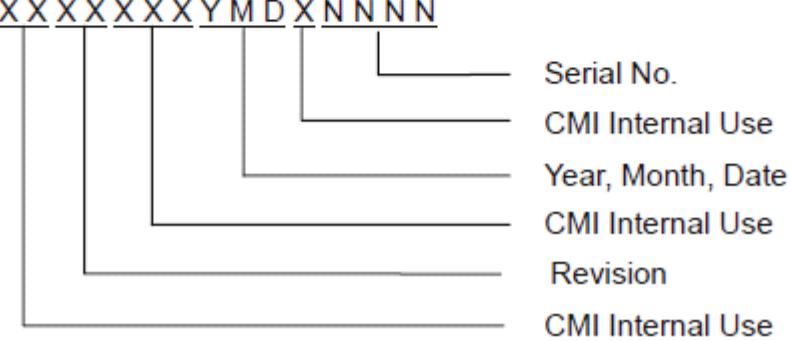
## 10. DEFINITION OF LABEL

### 10.1 CMI MODULE LABEL

The barcode nameplate is pasted on each module as illustration, and its definitions are as following explanation.



- (a) Model Name: G133IGE -L03
- (b) Revision: Rev. XX, for example: A1, ..., C1, C2 ...etc.
- (c) Serial ID: XXXXXXXXYMDXNNNN



Serial ID includes the information as below:

- (a) Manufactured Date: Year: 1~9, for 2001~2009
  - Month: 1~9, A~C, for Jan. ~ Dec.
  - Day: 1~9, A~Y, for 1<sup>st</sup> to 31<sup>st</sup>, exclude I , O and U
- (b) Revision Code: cover all the change
  - Serial No.: Manufacturing sequence of product

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## 11. PRECAUTIONS

### 11.1 ASSEMBLY AND HANDLING PRECAUTIONS

- (1) Do not apply rough force such as bending or twisting to the module during assembly.
- (2) To assemble or install module into user's system can be only in clean working areas. The dust and oil may cause electrical short or worsen the polarizer.
- (3) It's not permitted to have pressure or impulse on the module because the LCD panel and Backlight will be damaged.
- (4) Always follow the correct power sequence when LCD module is connecting and operating. This can prevent damage to the CMOS LSI chips during latch-up.
- (5) Do not pull the I/F connector in or out while the module is operating.
- (6) Do not disassemble the module.
- (7) Use a soft dry cloth without chemicals for cleaning, because the surface of polarizer is very soft and easily scratched.
- (8) It is dangerous that moisture come into or contacted the LCD module, because moisture may damage LCD module when it is operating.
- (9) High temperature or humidity may reduce the performance of module. Please store LCD module within the specified storage conditions.
- (10) When ambient temperature is lower than 10°C may reduce the display quality. For example, the response time will become slowly.
- (11) Do not keep same pattern in a long period of time. It may cause image sticking on LCD

### 11.2 SAFETY PRECAUTIONS

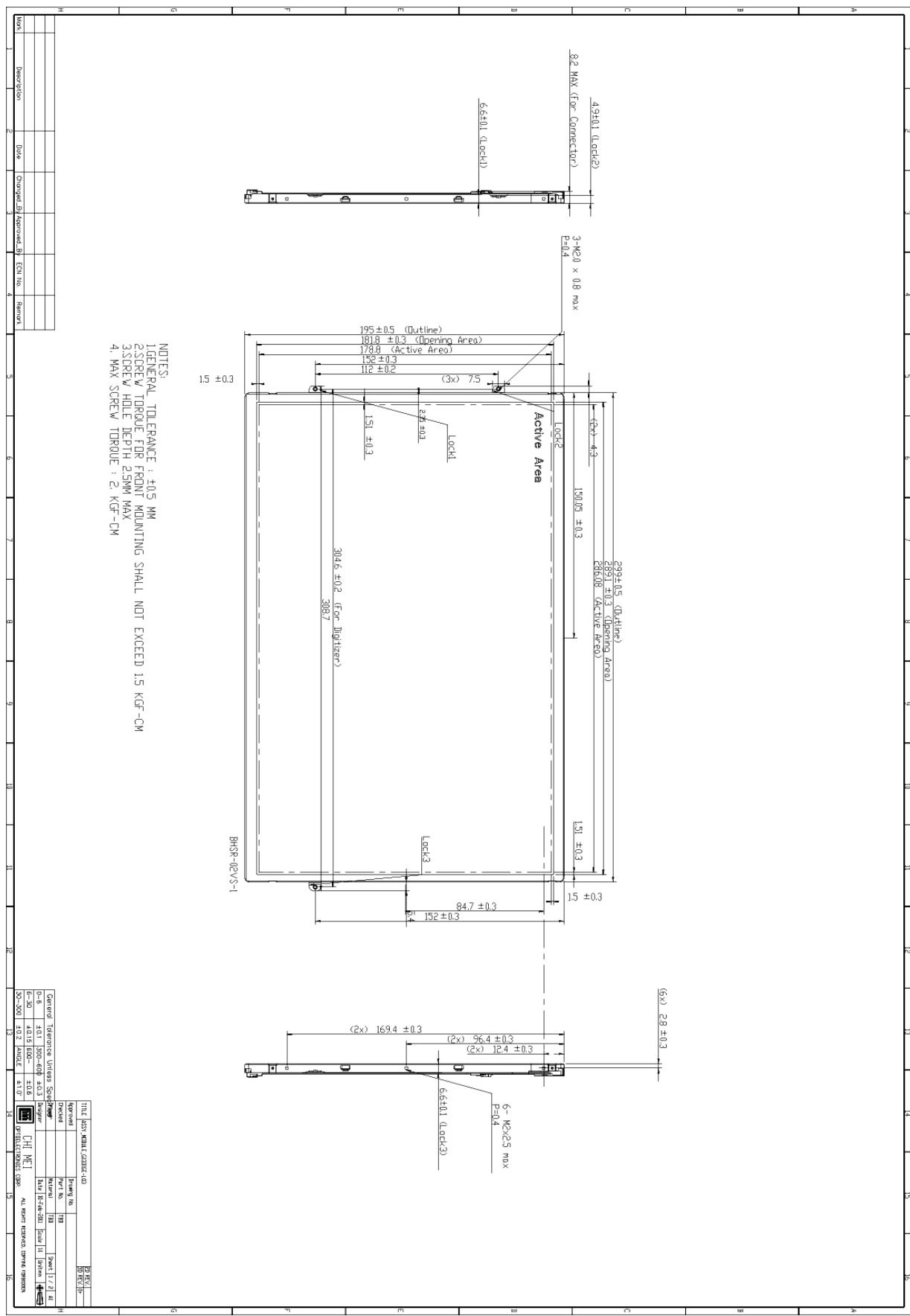
- (1) Do not disassemble the module or insert anything into the Backlight unit to prevent electrical shock.
- (2) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, skin or clothes, it has to be washed away thoroughly with soap.
- (3) After the module's end of life, it is not harmful in case of normal operation and storage.

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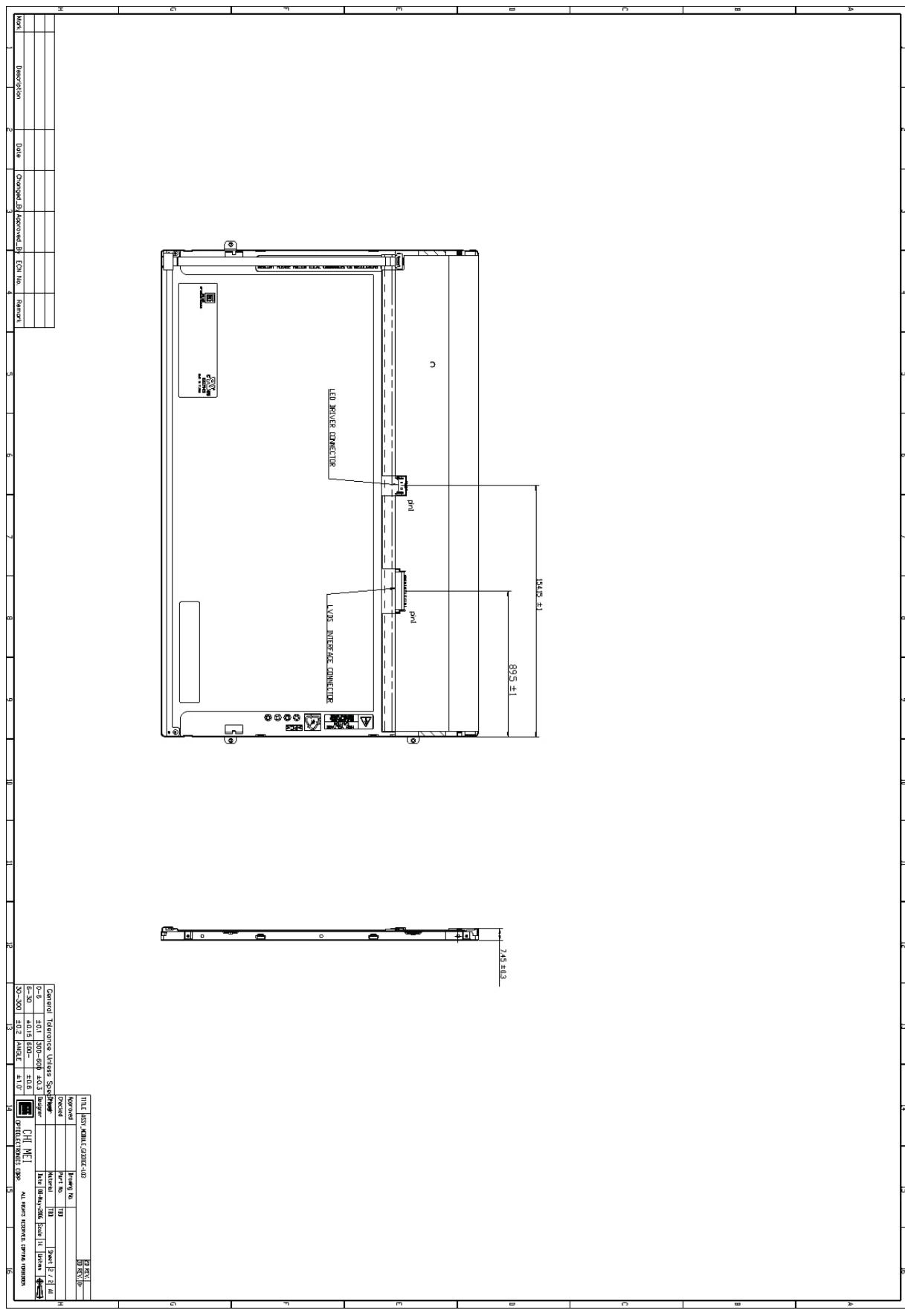


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